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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit:

Serial No.: 09/892,225 Examiner: Mathew J. Song

Filed: June 25, 2001 Confirmation No. 1969

Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information

Disclosure Statement and documents listed on form PTO-1449.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The document is in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosure plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Respectfully submitted,

Date:

Scott C. Harris Reg. No. 32,030

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